

APPARATUS AND METHOD FOR PERFORMING STATIC TIMING ANALYSIS OF AN
INTEGRATED CIRCUIT DESIGN USING DUMMY EDGE MODELING
DARSOW ET AL.
DOCKET NO. ROC920030338US1

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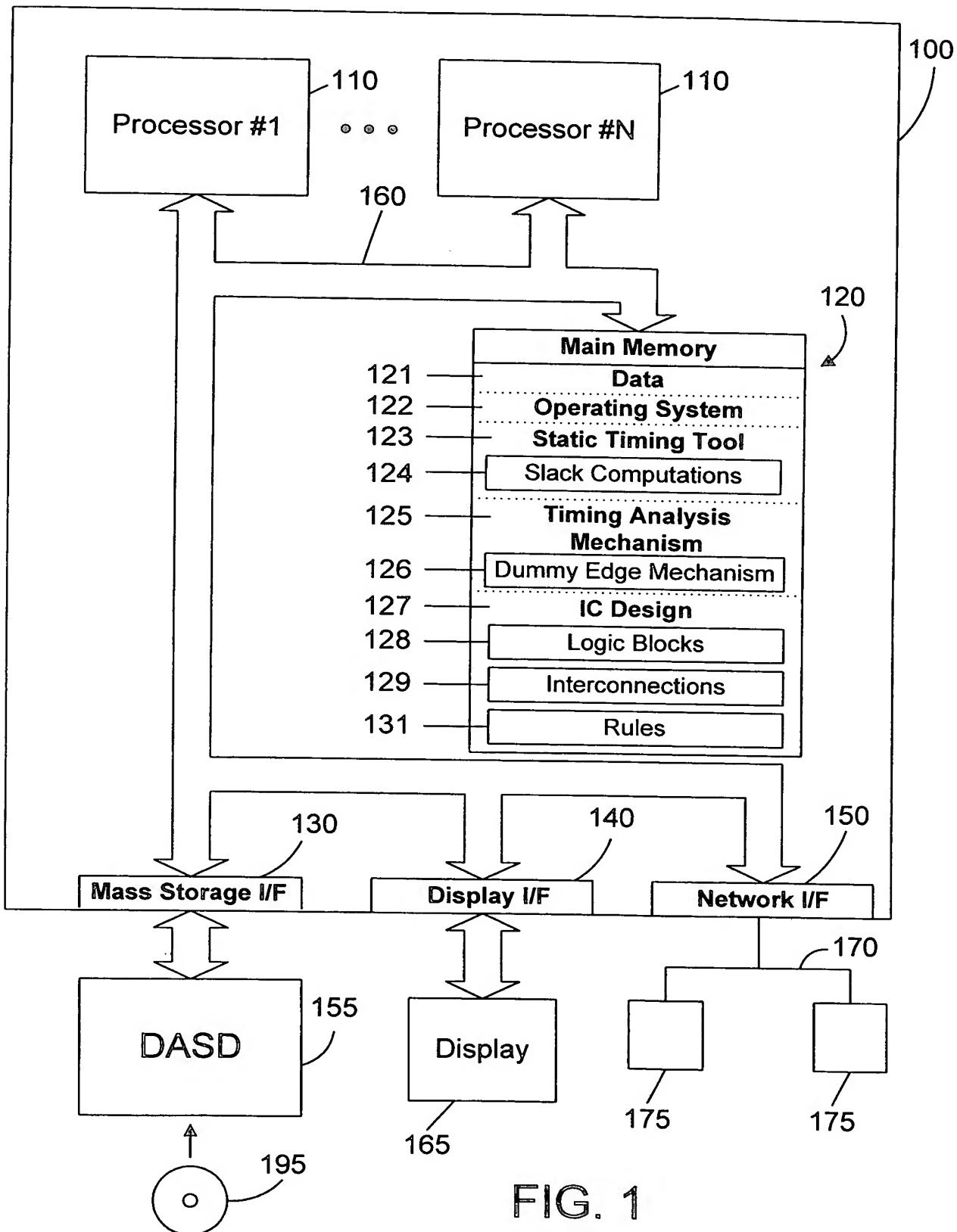


FIG. 1

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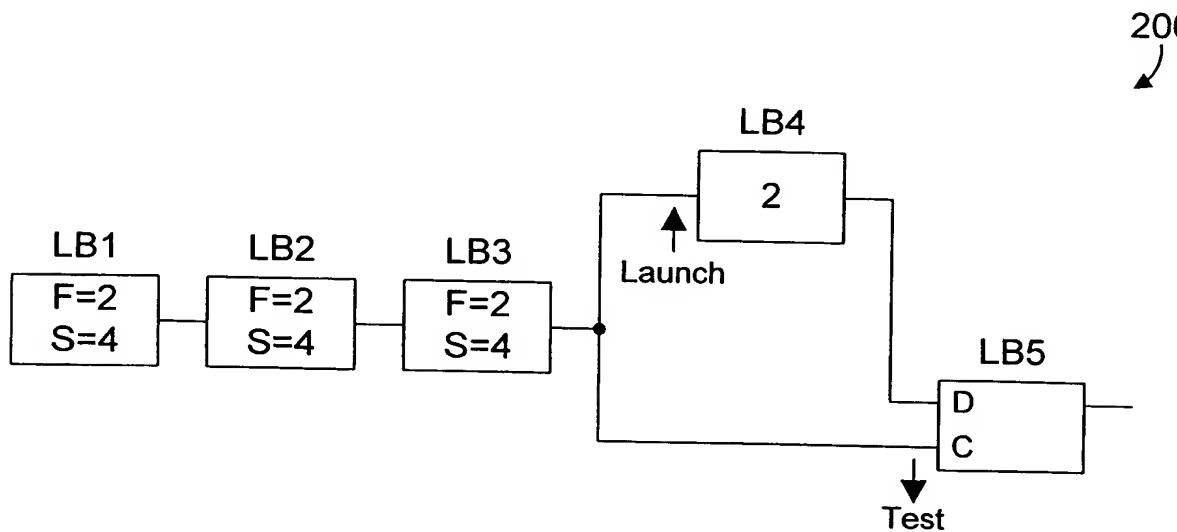


FIG. 2

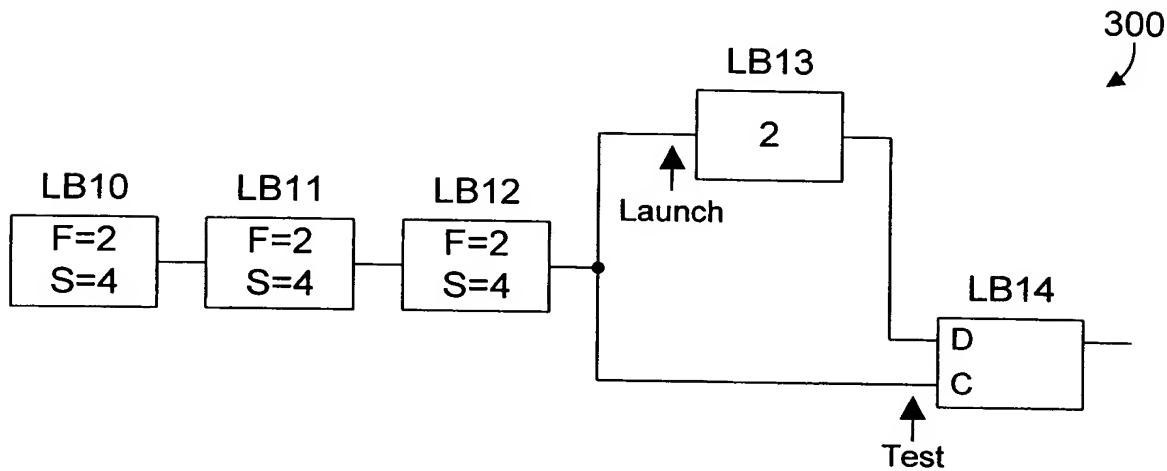


FIG. 3

APPARATUS AND METHOD FOR PERFORMING STATIC TIMING ANALYSIS OF AN
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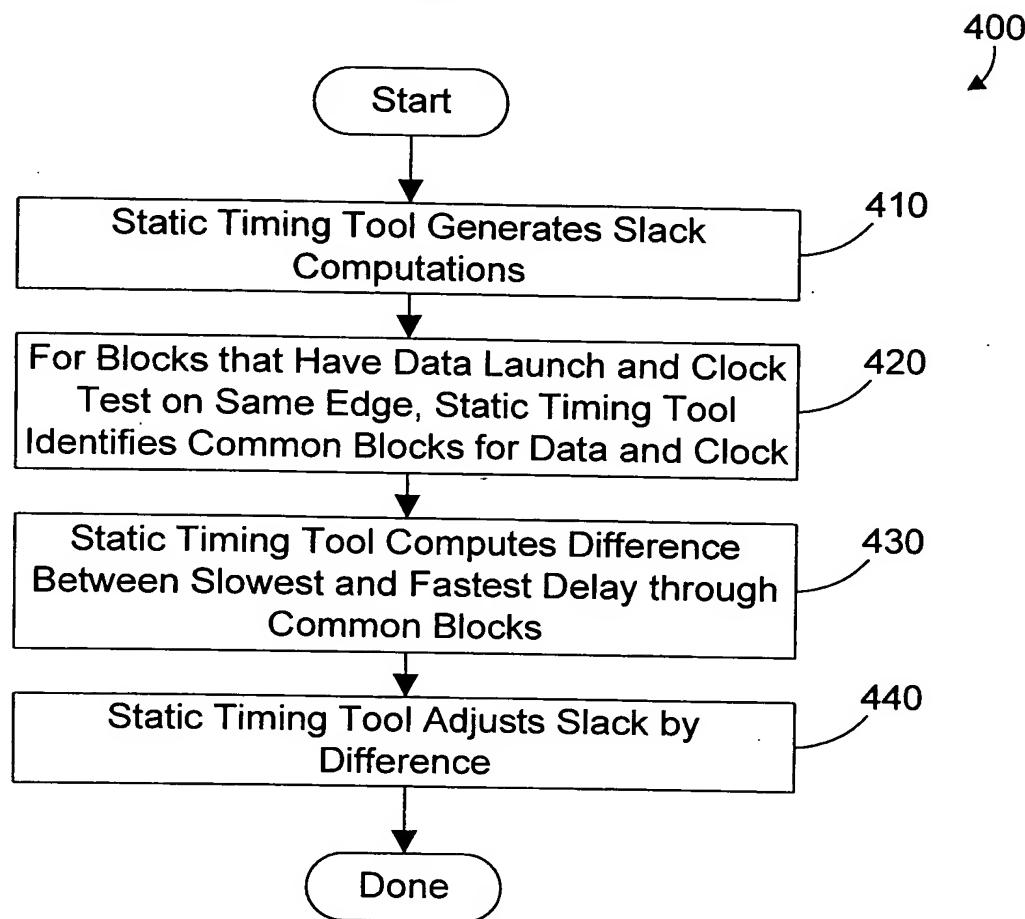


FIG. 4 Prior Art

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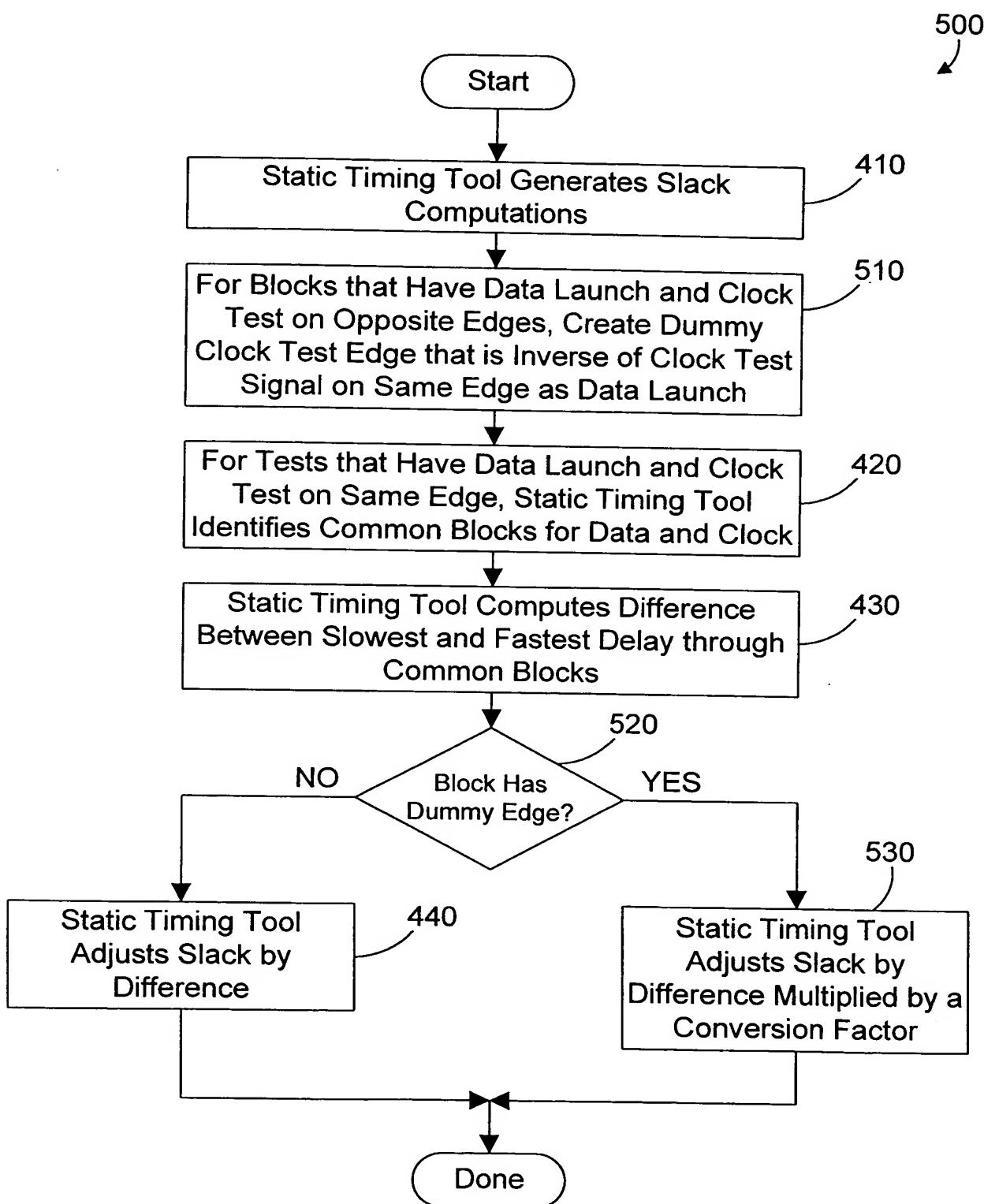


FIG. 5

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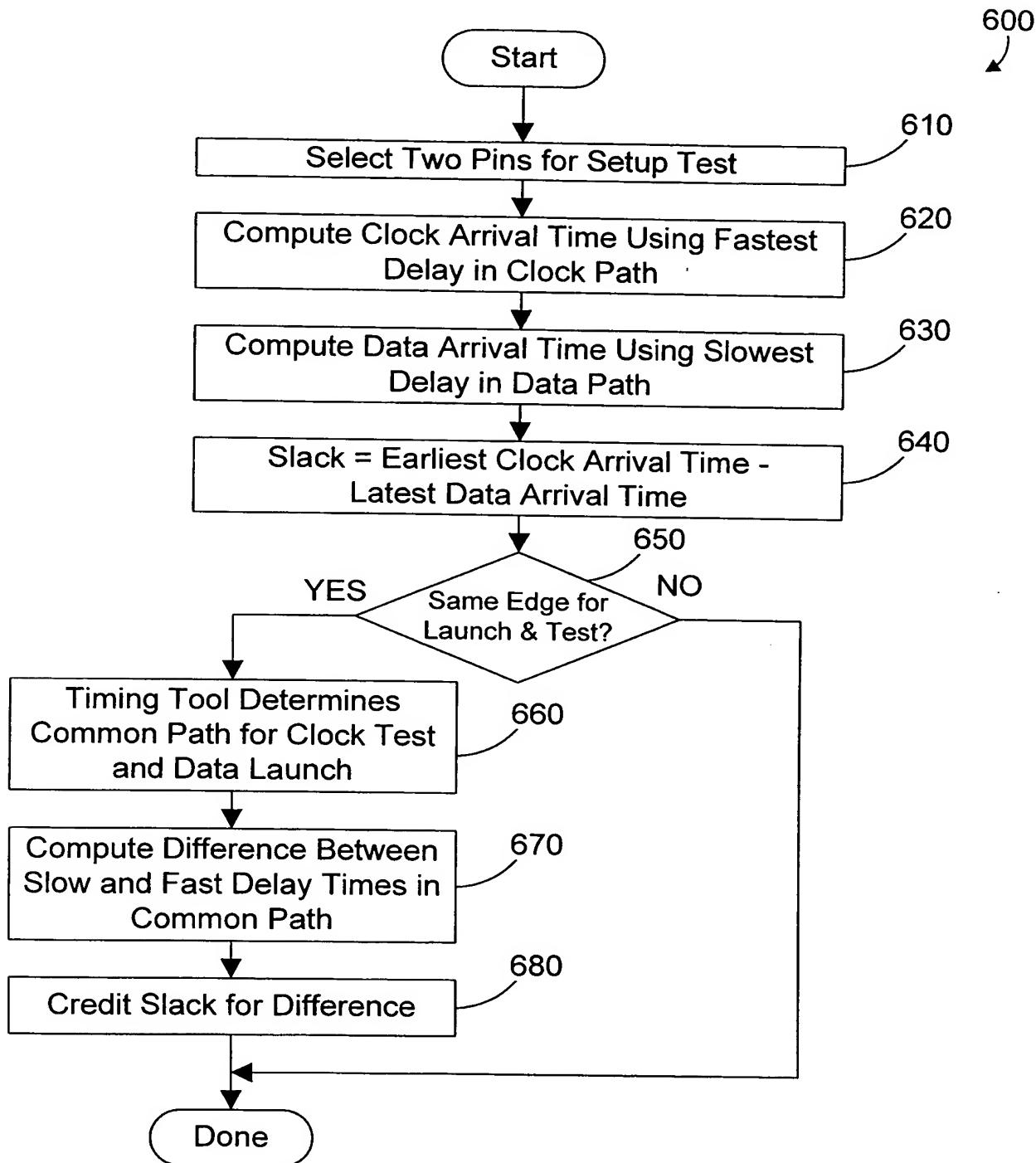
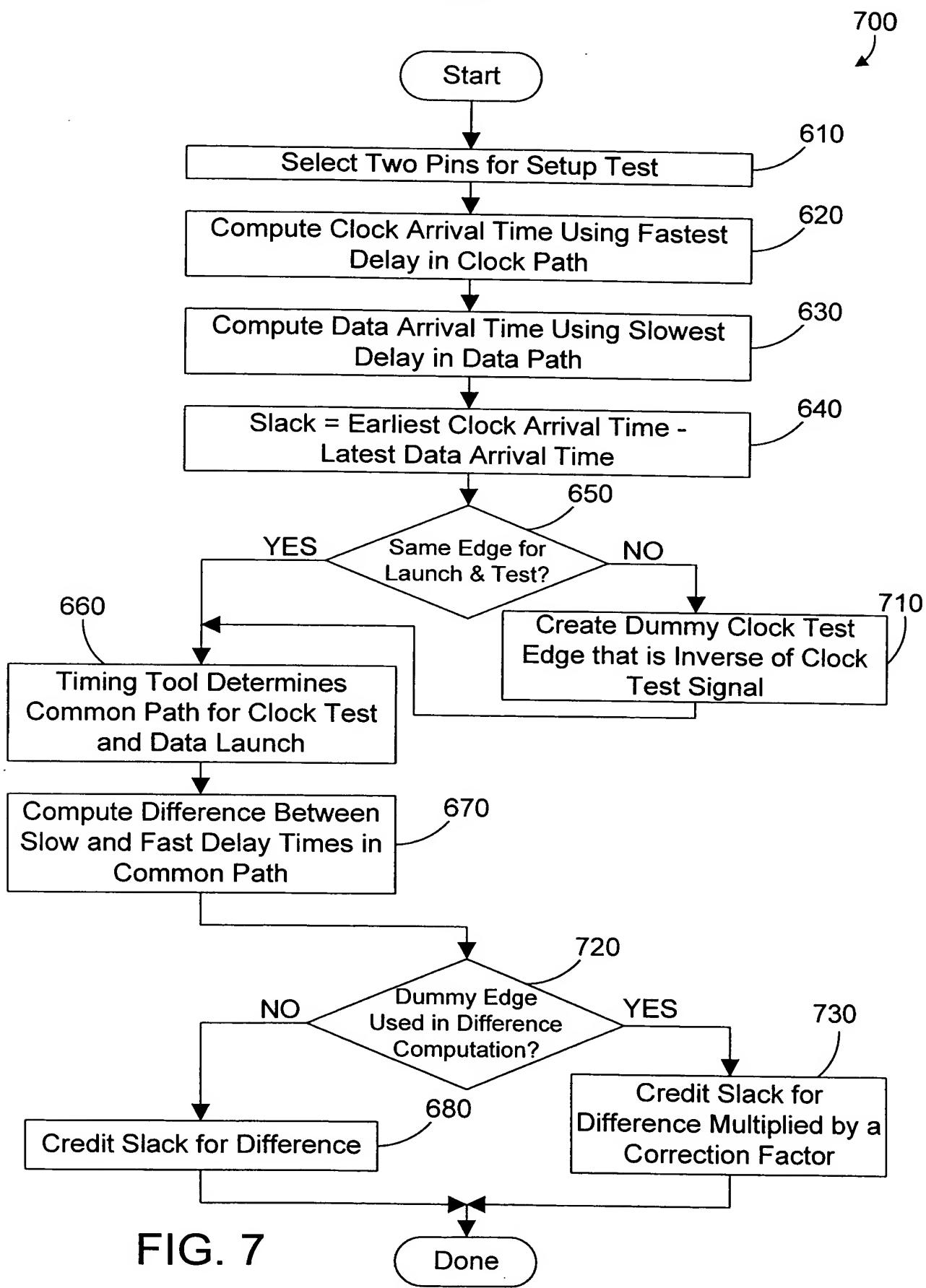


FIG. 6 Prior Art

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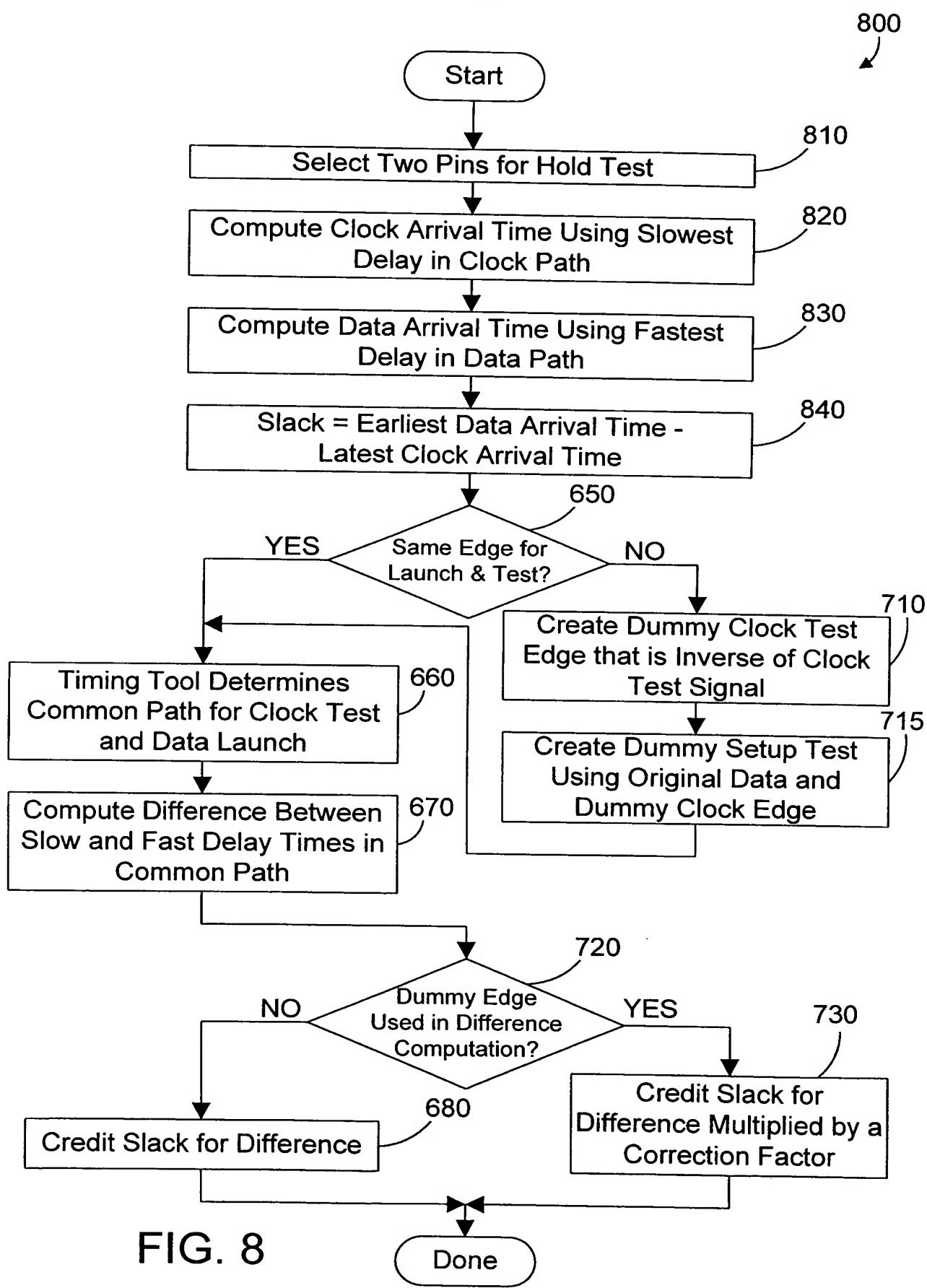


FIG. 8

APPARATUS AND METHOD FOR PERFORMING STATIC TIMING ANALYSIS OF AN
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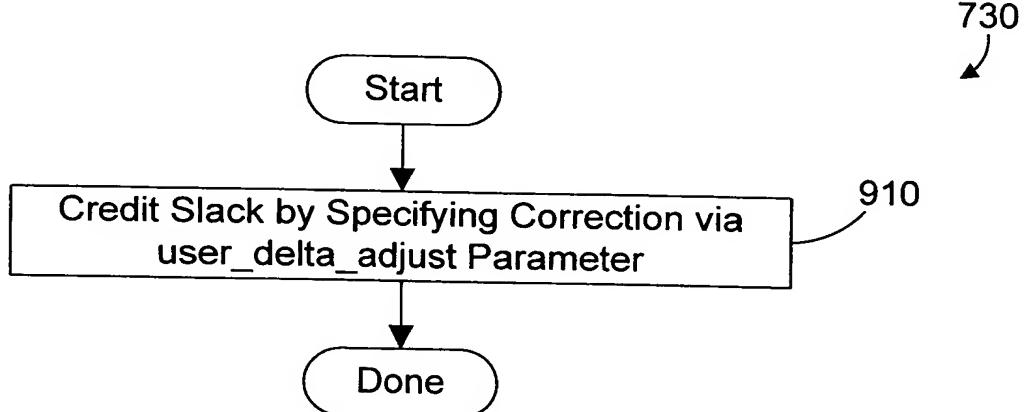


FIG. 9